INTEGRATED CIRCUITS

DATA SHEET

74ALS245A/74ALS245A–1Octal transceiver (3–State)

Product specification IC05 Data Handbook





Octal transceiver (3-State)

74ALS245A/74ALS245A-1

FEATURES

- Octal bidirectional bus interface
- 3-State buffer outputs sink 24mA and source 15mA
- Outputs are placed in high impedance state during power-off conditions
- The -1 version sinks 48mA

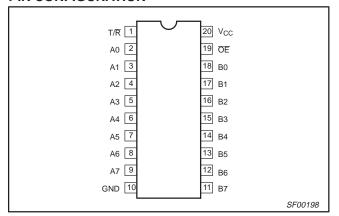
DESCRIPTION

The 74ALS245A is an octal transceiver featuring non-inverting 3-State bus compatible outputs in both transmit and receive directions. The device features an output enable (\overline{OE}) input for easy cascading and transmit/receive (R/\overline{I}) input for direction control.

The 74ALS245A-1 is the same as the 74ALS245A except that both ports sink 48mA within the $\pm5\%~V_{CC}$ range.

TYPE	TYPICAL PROPAGATION DELAY	TYPICAL SUPPLY CURRENT (TOTAL)
74ALS245A	7.0ns	34mA
74ALS245A-1	7.0ns	34mA

PIN CONFIGURATION



ORDERING INFORMATION

	ORDER CODE	
DESCRIPTION	COMMERCIAL RANGE V_{CC} = 5V ±10%, T_{amb} = 0°C to +70°C	DRAWING NUMBER
20-pin plastic DIP	74ALS245AN, 74ALS245A-1N	SOT146-1
20-pin plastic SOL	74ALS245AD, 744ALS245A-1D	SOT163-1
20-pin plastic SSOP Type II	74ALS245ADB, 74ALS245A-1DB	SOT339-1

INPUT AND OUTPUT LOADING AND FAN-OUT TABLE

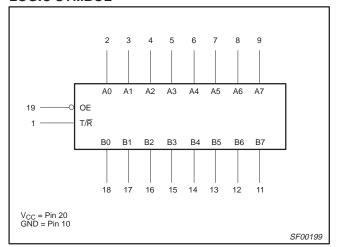
PINS	DESCRIPTION	74ALS (U.L.) HIGH/LOW	LOAD VALUE HIGH/LOW
A0 – A7, B0 – B7	Data inputs	1.0/1.0	20μA/0.1mA
ŌĒ	Output Enable input (active-Low)	1.0/1.0	20μA/0.1mA
T/R	Transmit/receive input	1.0/1.0	20μA/0.1mA
A0 – A7	A port outputs	750/240	15mA/24mA
B0 – B7	B port outputs	750/240	15mA/24mA
A0 – A7	A port outputs (-1 version)	750/480	15mA/48mA
B0 – B7	B port outputs (-1 version)	750/480	15mA/48mA

NOTE: One (1.0) ALS unit load is defined as: $20\mu A$ in the High state and 0.1mA in the Low state.

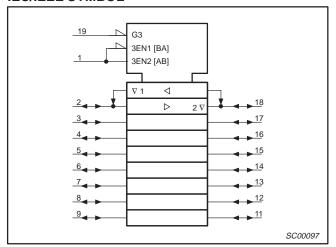
Octal transceiver (3-State)

74ALS245A/74ALS245A-1

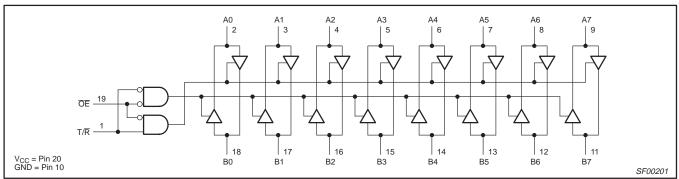
LOGIC SYMBOL



IEC/IEEE SYMBOL



LOGIC DIAGRAM



FUNCTION TABLE

INP	JTS	OUTPUTS
ŌĒ	T/R	0017013
L	L	Bus B data to Bus A
L	Н	Bus A data to Bus B
Н	Х	Z

H = High voltage level L = Low voltage level

X = Don't care Z = High impedance "off" state

Octal transceiver (3-State)

74ALS245A/74ALS245A-1

ABSOLUTE MAXIMUM RATINGS

(Operation beyond the limit set forth in this table may impair the useful life of the device. Unless otherwise noted these limits are over the operating free air temperature range.)

SYMBOL	PARAMETER	RATING	UNIT	
V _{CC}	Supply voltage		-0.5 to +7.0	V
V_{IN}	Input voltage		-0.5 to +7.0	V
I _{IN}	Input current	-30 to +5	mA	
V _{OUT}	Voltage applied to output in High output state	–0.5 to V _{CC}	V	
	Current applied to output in Low output state	All versions	48	mA
lout	Current applied to output in Low output state	96	mA	
T _{amb}	Operating free-air temperature range	0 to +70	°C	
T _{stg}	Storage temperature range	-65 to +150	°C	

RECOMMENDED OPERATING CONDITIONS

SYMBOL				LIMITS			
STIVIBUL	PARAMETER		MIN	NOM	MAX	UNIT	
V _{CC}	Supply voltage		4.5	5.0	5.5	V	
V _{IH}	High-level input voltage		2.0			V	
V _{IL}	Low-level input voltage			0.8	V		
I _{IK}	Input clamp current				-18	mA	
I _{OH}	High-level output current				-15	mA	
la.	Low lovel output ourrent	All versions			24	mA	
l _{OL}	Low-level output current			48 ¹	mA		
T _{amb}	Operating free-air temperature range	0		+70	°C		

NOTES:

^{1.} The 48mA limit applies only under the condition of V_{CC} = 5.0V $\pm\,5\%.$

Octal transceiver (3-State)

74ALS245A/74ALS245A-1

DC ELECTRICAL CHARACTERISTICS

(Over recommended operating free-air temperature range unless otherwise noted.)

OVMDOL	DADAMETER		TEST SONDITI	ONO1		LIMITS		LINUT
SYMBOL	PARAMETER	₹	IESI CONDIII	TEST CONDITIONS ¹			MAX	UNIT
			V _{CC} ±10%, V _{IL} = MAX,	$I_{OH} = -0.4$ mA	V _{CC} - 2			V
V_{OH}	High-level output voltage		V _{IH} = MIN	$I_{OH} = -3mA$	2.4	3.2		V
On			$V_{CC} = MIN, V_{IL} = MAX, V_{IH} = MIN$	I _{OH} = -15mA	2.0			V
		All versions	V _{CC} = MIN, V _{IL} = MAX,	$I_{OL} = 12mA$		0.25	0.40	V
V_{OL}	Low-level output voltage	All versions	V _{IH} = MIN	I _{OL} = 24mA		0.35	0.50	V
- OL		-1 version	$V_{CC} = 4.75V$, $V_{IL} = MAX$, $V_{IH} = MIN$	I _{OL} = 48mA		0.35	0.50	V
V _{IK}	Input clamp voltage		$V_{CC} = MIN, I_I = I_{IK}$			-0.73	-1.5	V
,	Input current at maxi-	OE or T/R	$V_{CC} = MAX, V_I = 7.0V$			0.1	mA	
t _l	mum input voltage	A or B ports	$V_{CC} = MAX, V_I = 5.5V$				0.1	mA
I _{IH}	High-level input current ³		$V_{CC} = MAX, V_I = 2.7V$				20	μΑ
I _{IL}	Low-level input current ³		$V_{CC} = MAX, V_I = 0.4V$				-0.1	mA
Io	Output current ⁴		$V_{CC} = MAX, V_O = 2.25V$		-30		-112	mA
		I _{CCH}				28	45	mA
Icc	Supply current (total)	I _{CCL}	V _{CC} = MAX			40	55	mA
		I _{CCZ}				44	58	mA

- For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions for the applicable type.
 All typical values are at V_{CC} = 5V, T_{amb} = 25°C.
 For I/O ports, the parameter I_{IH} and I_{IL} include the off-state current.
 The output conditions have been chosen to produce a current that closely approximates one half of the true short-circuit output current, I_{OS}.

AC ELECTRICAL CHARACTERISTICS

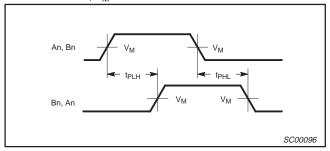
SYMBOL			LIMITS				
	PARAMETER	TEST CONDITION	T _{amb} = 0°0 V _{CC} = +5. C _L = 50pF,	C to +70°C 0V ± 10% R _L = 500Ω	UNIT		
			MIN	MAX			
t _{PLH} t _{PHL}	Propagation delay An to Bn, Bn to An	Waveform 1	2.0 2.0	10.0 10.0	ns		
t _{PZH}	Output enable time to High or Low level	Waveform 2 Waveform 3	3.0 3.0	20.0 20.0	ns		
t _{PHZ}	Output disable time from High or Low level	Waveform 2 Waveform 3	2.0 4.0	10.0 15.0	ns		

Octal transceiver (3-State)

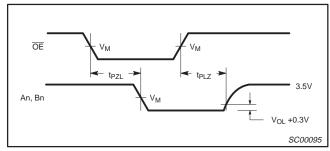
74ALS245A/74ALS245A-1

AC WAVEFORMS

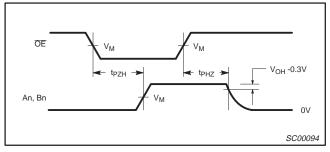
For all waveforms, $V_M = 1.3V$.



Waveform 1. Propagation Delay for Non-inverting Outputs

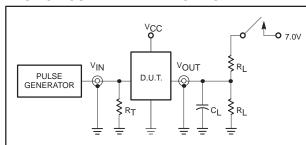


Waveform 3. 3-State Output Enable Time to Low Level and Output Disable Time from Low Level



Waveform 2. 3-State Output Enable Time to High Level and Output Disable Time from High Level

TEST CIRCUIT AND WAVEFORMS



Test Circuit for 3-State Outputs

SWITCH POSITION

TEST	SWITCH
t _{PLZ} , t _{PZL}	closed
All other	open

DEFINITIONS:

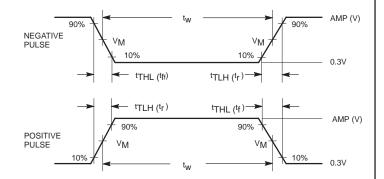
R_L = Load resistor;

see AC electrical characteristics for value.

L = Load capacitance includes jig and probe capacitance;

see AC electrical characteristics for value.

R_T = Termination resistance should be equal to Z_{OUT} of pulse generators.



Input Pulse Definition

Comily		INPUT	PULSE RE	QUIREN	MENTS	
Family	Amplitude	V_{M}	Rep.Rate	t _w	t _{TLH}	t _{THL}
74ALS	3.5V	1.3V	1MHz	500ns	2.0ns	2.0ns

SC00072

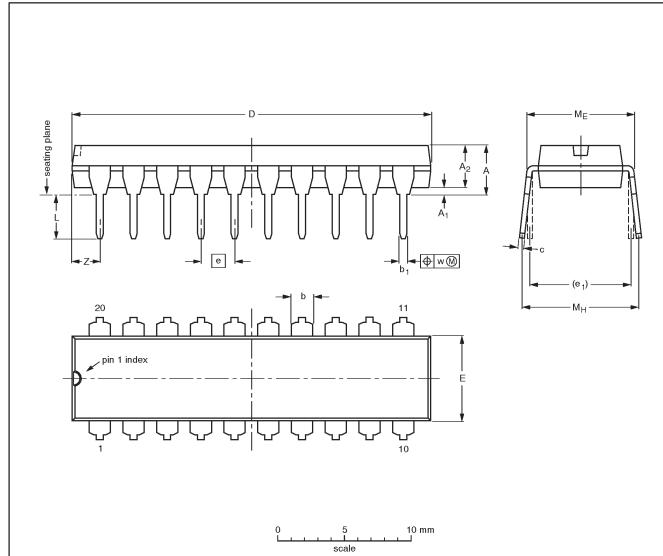
1991 Jun 03 6

Octal transceiver (3-State)

74ALS245A/74ALS245A-1

DIP20: plastic dual in-line package; 20 leads (300 mil)

SOT146-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁ min.	A ₂ max.	b	b ₁	С	D ⁽¹⁾	E ⁽¹⁾	е	e ₁	L	ME	M _H	w	Z ⁽¹⁾ max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	0.36 0.23	26.92 26.54	6.40 6.22	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.0
inches	0.17	0.020	0.13	0.068 0.051	0.021 0.015	0.014 0.009	1.060 1.045	0.25 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.078

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFER	RENCES	EUROPEAN	ISSUE DATE	
	VERSION	IEC	JEDEC	EIAJ	PROJECTION	
	SOT146-1			SC603		92-11-17 95-05-24

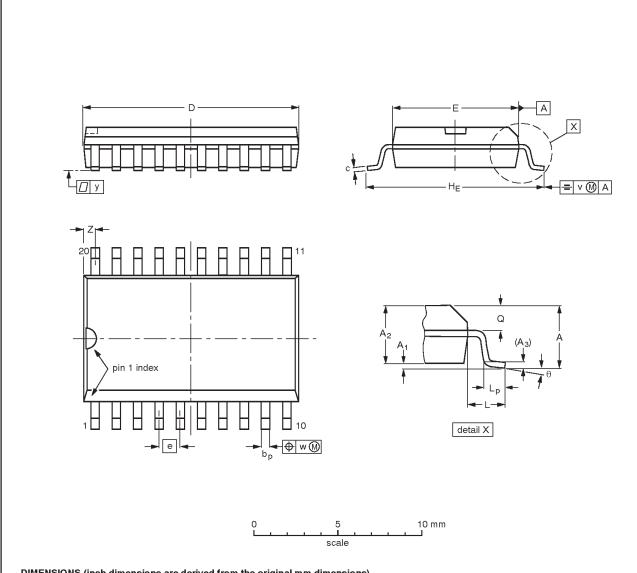
1991 Jun 03 7

Octal transceiver (3-State)

74ALS245A/74ALS245A-1

SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	bp	O	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	v	w	у	z ⁽¹⁾	θ
mm	2.65	0.30 0.10	2.45 2.25	0.25	0.49 0.36	0.32 0.23	13.0 12.6	7.6 7.4	1.27	10.65 10.00	1.4	1.1 0.4	1.1 1.0	0.25	0.25	0.1	0.9 0.4	8°
inches	0.10	0.012 0.004	0.096 0.089	0.01	0.019 0.014	0.013 0.009	0.51 0.49	0.30 0.29	0.050	0.42 0.39	0.055	0.043 0.016	0.043 0.039	0.01	0.01	0.004	0.035 0.016	o°

Note

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.

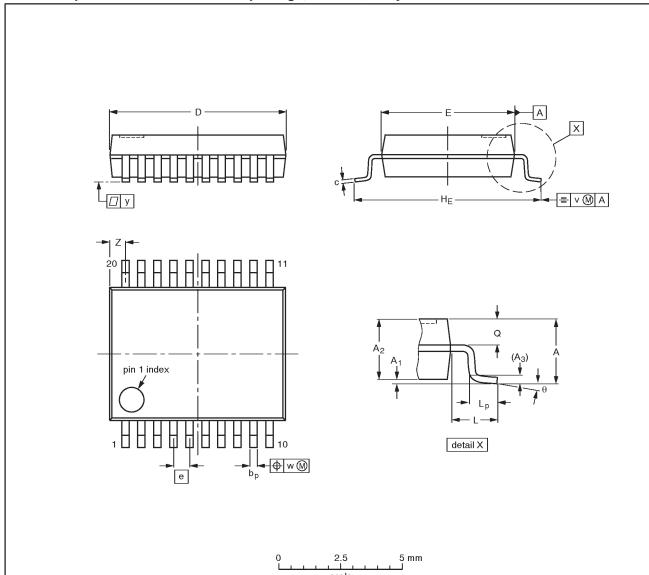
OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	1930E DATE	
SOT163-1	075E04	MS-013AC				-92-11-17 95-01-24	

Octal transceiver (3-State)

74ALS245A/74ALS245A-1

SSOP20: plastic shrink small outline package; 20 leads; body width 5.3 mm

SOT339-1



DIMENSIONS (mm are the original dimensions)

UNIT	A max.	Α1	A ₂	A ₃	bр	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Ø	v	w	у	Z ⁽¹⁾	θ
mm	2.0	0.21 0.05	1.80 1.65	0.25	0.38 0.25	0.20 0.09	7.4 7.0	5.4 5.2	0.65	7.9 7.6	1.25	1.03 0.63	0.9 0.7	0.2	0.13	0.1	0.9 0.5	8° 0°

Note

1. Plastic or metal protrusions of 0.20 mm maximum per side are not included.

OUTLINE		EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT339-1		MO-150AE				93-09-08 95-02-04

Octal transceiver (3-State)

74ALS245A/74ALS245A-1

	DEFINITIONS						
Data Sheet Identification	Product Status	Definition					
Objective Specification	Formative or in Design	This data sheet contains the design target or goal specifications for product development. Specifications may change in any manner without notice.					
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